

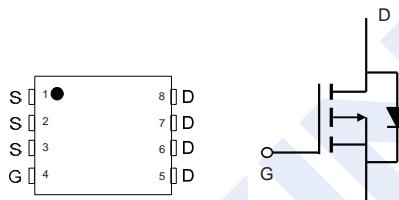
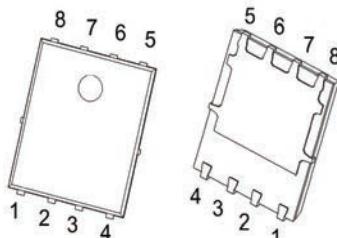
P-Channel MOSFET

2KJ6059DFN

■ Features

- V_{DS} -40 V
- I_D (at $V_{GS} = -10V$) -35 A
- $R_{DS(ON)}$ (at $V_{GS} = -10V$) < 12.5 mΩ
- $R_{DS(ON)}$ (at $V_{GS} = -4.5V$) < 18.5 mΩ
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge

PDFN5x6-8

■ Absolute Maximum Ratings ($T_c = 25^\circ C$ unless otherwise noted.)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage	$T_c = 25^\circ C$	V_{DS}	-40	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current	$T_c = 25^\circ C$	I_D	-35	A
	$T_c = 100^\circ C$		-23	
Pulsed Drain Current (Note 1)		I_{DM}	-140	
Single Pulsed Avalanche Energy (Note 2)		E_{AS}	132	
Power Dissipation		P_D	26	W
Thermal Resistance, Junction- to-Case		R_{eJC}	4.8	°C/W
Junction Temperature		T_J	150	
Storage Temperature Range		T_{stg}	-55 to 150	°C

Notes:

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature
2. EAS condition: $T_J = 25^\circ C$, $V_{DD} = -20V$, $V_G = -10V$, $L = 0.5mH$, $R_G = 25\Omega$, $I_{AS} = -23A$

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■ Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise noted)

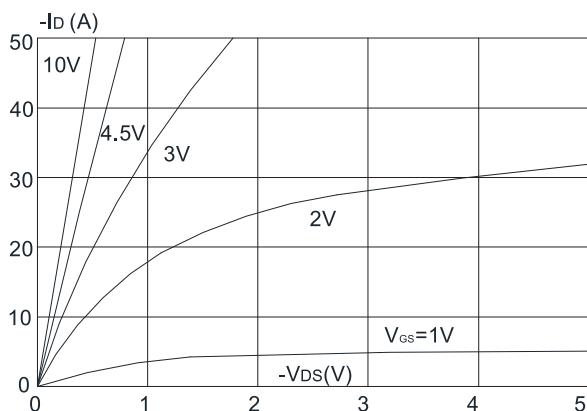
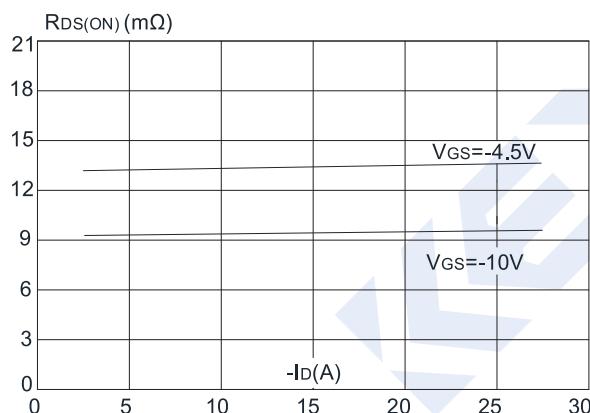
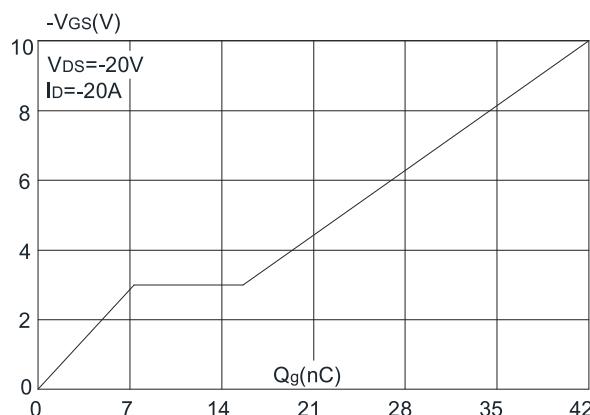
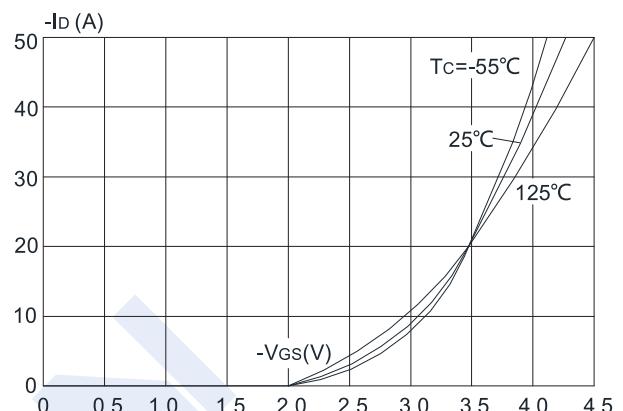
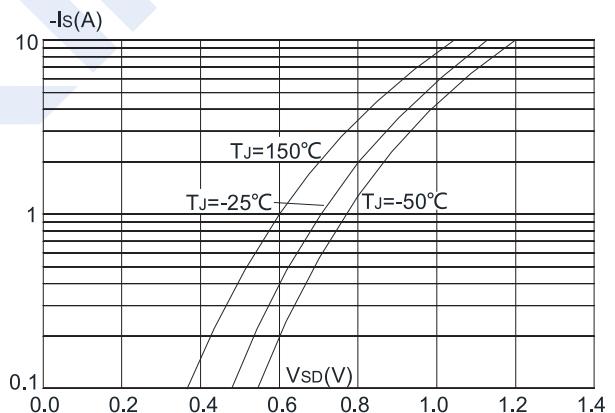
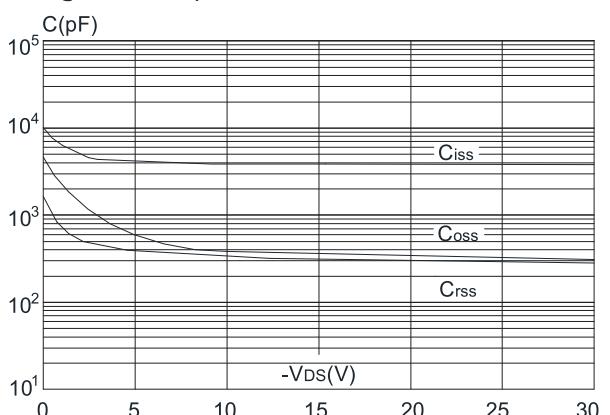
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$\text{Id} = -250\mu\text{A}, \text{V}_{\text{GS}} = 0\text{V}$	-40			V
Zero Gate Voltage Drain Current	Id_{SS}	$\text{V}_{\text{DS}} = -40\text{V}, \text{V}_{\text{GS}} = 0\text{V}$			-1	μA
		$\text{V}_{\text{DS}} = -40\text{V}, \text{V}_{\text{GS}} = 0\text{V}, \text{T}_J = 55^\circ\text{C}$			-25	
Gate-Body Leakage Current	I_{GSS}	$\text{V}_{\text{DS}} = 0\text{V}, \text{V}_{\text{GS}} = \pm 20\text{V}$			± 100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$\text{V}_{\text{GS(th)}}$	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{Id} = -250\mu\text{A}$	-1.0		-2.5	V
Static Drain-Source On-Resistance (Note 3)	$\text{R}_{\text{DS(on)}}$	$\text{V}_{\text{GS}} = -10\text{V}, \text{Id} = -20\text{A}$			12.5	$\text{m}\Omega$
		$\text{V}_{\text{GS}} = -4.5\text{V}, \text{Id} = -10\text{A}$			18.5	
DYNAMIC CHARACTERISTICS (Note 4)						
Input Capacitance	C_{iss}	$\text{V}_{\text{GS}} = 0\text{V}, \text{V}_{\text{DS}} = -20\text{V}, \text{f} = 1\text{MHz}$		3800		pF
Output Capacitance	C_{oss}			329		
Reverse Transfer Capacitance	C_{rss}			289		
Total Gate Charge	Q_{g}	$\text{V}_{\text{DS}} = -20\text{V}, \text{V}_{\text{GS}} = -10\text{V}, \text{Id} = -20\text{A}$		42		nC
Gate Source Charge	Q_{gs}			7.3		
Gate Drain Charge	Q_{gd}			8.5		
SWITCHING CHARACTERISTICS (Note 5)						
Turn-On Delay Time	$\text{t}_{\text{d(on)}}$	$\text{V}_{\text{DD}} = -20\text{V}, \text{R}_{\text{GEN}} = 2.5\Omega, \text{Id} = -20\text{A}, \text{V}_{\text{GS}} = -10\text{V}$		10		ns
Turn-On Rise Time	t_r			21		
Turn-Off Delay Time	$\text{t}_{\text{d(off)}}$			53		
Turn-Off Fall Time	t_f			29		
DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Maximum Body-Diode Continuous Current	I_{s}				-35	A
Maximum Body-Diode Pulsed Current	I_{SM}				-140	
Reverse Recovery Time	t_{rr}	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_{\text{s}} = -30\text{A}, \text{di/dt} = 100\text{A}/\mu\text{s}$		39		ns
Reverse Recovery Charge	Q_{rr}			42		μC
Diode Forward Voltage	V_{SD}	$\text{I}_{\text{s}} = -35\text{A}, \text{V}_{\text{GS}} = 0\text{V}$		-0.8	-1.2	V

Notes:

3. Measured under pulsed conditions. Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.
4. For design aid only, not subject to production testing.
5. Switching characteristics are independent of operating junction temperatures.

■ Marking

Marking	J6059 KC****
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P-Channel MOSFET**2KJ6059DFN****■ Typical Characteristics****Figure 1:** Output Characteristics**Figure 3:** On-resistance vs. Drain Current**Figure 5:** Gate Charge Characteristics**Figure 2:** Typical Transfer Characteristics**Figure 4:** Body Diode Characteristics**Figure 6:** Capacitance Characteristics

P-Channel MOSFET**2KJ6059DFN**

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

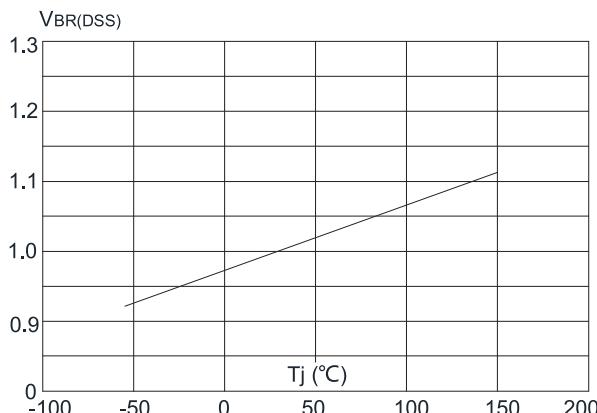


Figure 9: Maximum Safe Operating Area

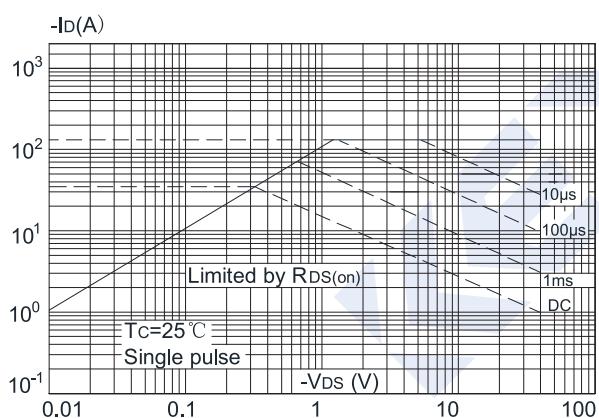


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

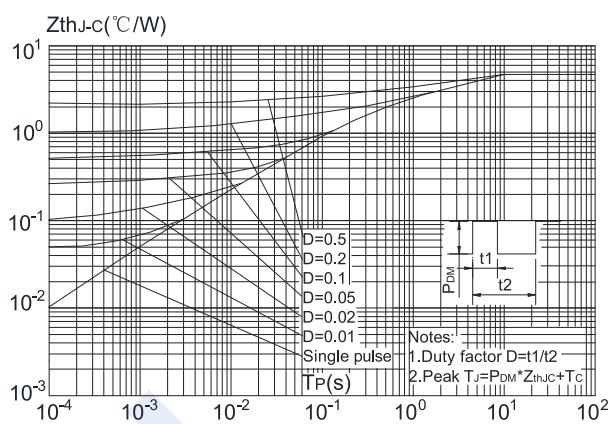


Figure 8: Normalized on Resistance vs. Junction Temperature

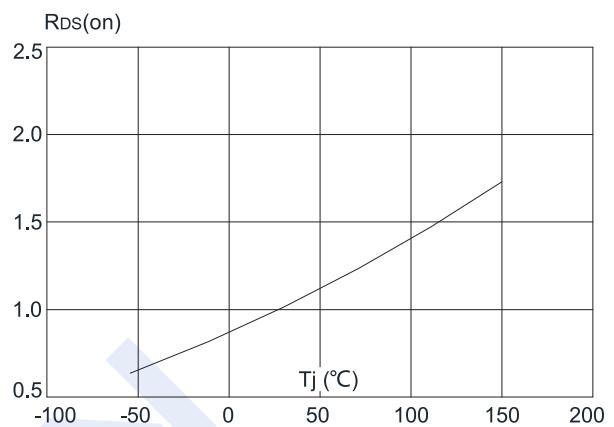
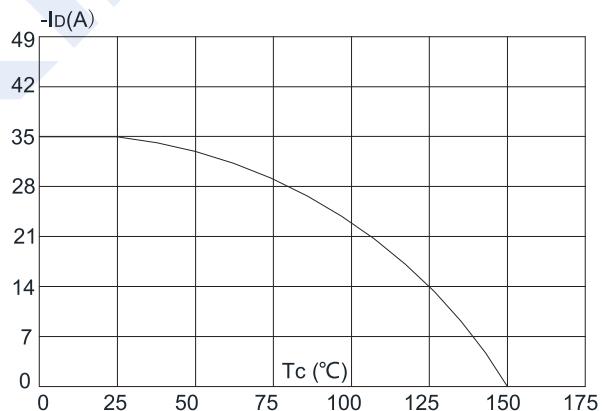


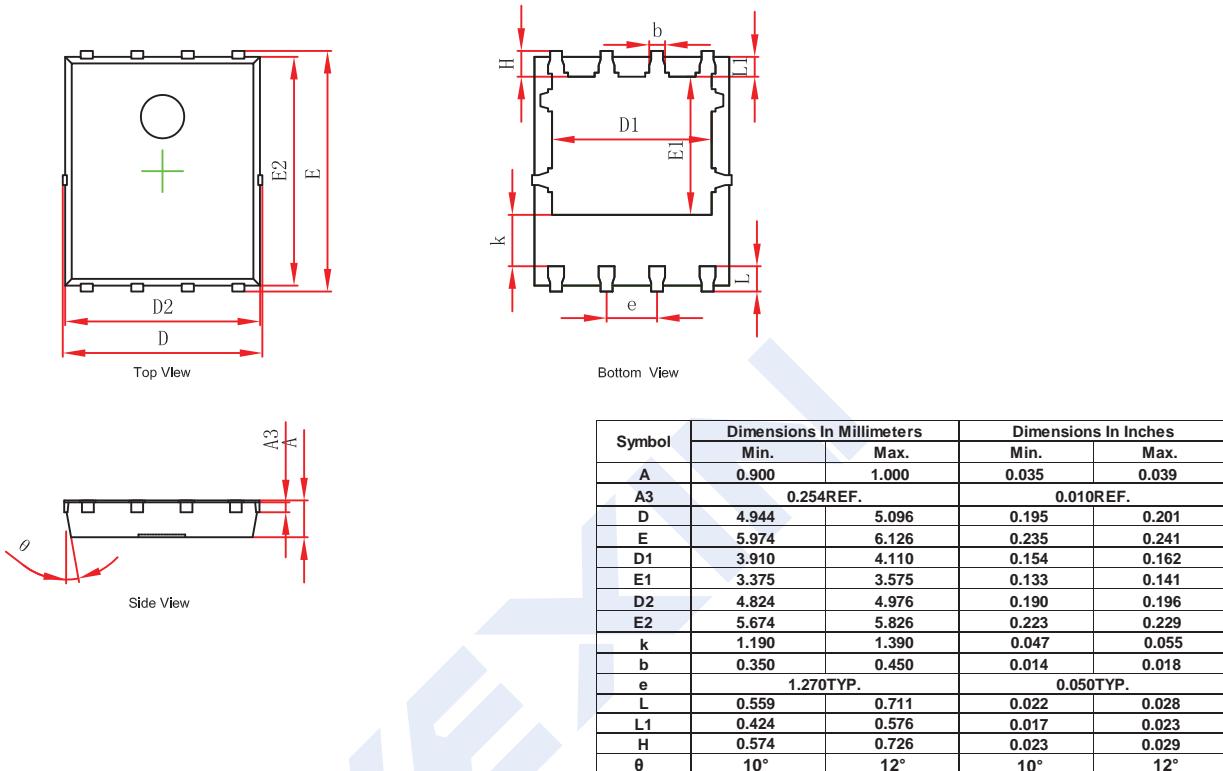
Figure 10: Maximum Continuous Drain Current vs. Case Temperature



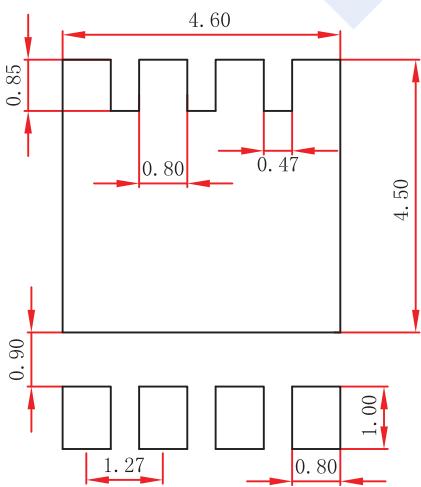
P-Channel MOSFET

2KJ6059DFN

■ PDFN5x6-8 Package Outline Dimensions



■ PDFN5x6-8 Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: $\pm 0.05\text{mm}$.
3. The pad layout is for reference purposes only.