

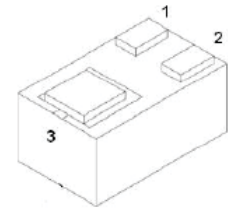
## N-Channel MOSFET

## 2KK6009DFN

## ■ Features

- $V_{DS} (V) = 20 V$
- $I_D = 0.75 A$
- $R_{DS(ON)} (at V_{GS} = 4.5 V) < 380 m\Omega$
- $R_{DS(ON)} (at V_{GS} = 2.5 V) < 450 m\Omega$
- $R_{DS(ON)} (at V_{GS} = 1.8 V) < 800 m\Omega$

DFN1006-3



- 1.GATE
- 2.SOURCE
- 3.DRAIN

■ Absolute Maximum Ratings ( $T_a = 25^\circ C$  unless otherwise noted)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	
Continuous Drain Current (Note 1)	$I_D$	0.75	A
Pulsed Drain Current ( $t_p=10\mu s$ )	$I_{DM}$	1.8	
Power Dissipation (Note 1)	$P_D$	100	mW
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	1250	$^\circ C/W$
Junction Temperature	$T_J$	150	$^\circ C$
Storage Temperature Range	$T_{stg}$	-55 to 150	

Note 1.Surface mounted on FR4 board using the minimum recommended pad size.

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## ■ Electrical Characteristics (Ta = 25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>STATIC PARAMETERS</b>						
Drain-Source Breakdown Voltage	BVDSS	Id = 250 $\mu$ A, VGS = 0V	20			V
Zero Gate Voltage Drain Current	IDSS	VDS = 20 V, VGS = 0 V			1	$\mu$ A
Gate to Source Leakage Current	IGSS	VDS = 0 V, VGS = $\pm$ 10 V			$\pm$ 20	
Gate to Source Threshold Voltage (Note 2)	VGS(th)	VDS = VGS, Id = 250 $\mu$ A	0.35		1.1	V
Static Drain-Source On-Resistance (Note 2)	RDS(on)	VGS = 4.5 V, Id = 0.65 A			380	m $\Omega$
		VGS = 2.5 V, Id = 0.55 A			450	
		VGS = 1.8 V, Id = 0.45 A			800	
Forward Transconductance (Note 2)	gFS	VDS = 10 V, Id = 0.8 A		1.6		S
Diode Forward Voltage	VSD	VGS = 0 V, IS = 0.15 A			1.2	V
<b>DYNAMIC PARAMETERS (Note 4)</b>						
Input Capacitance	Ciss	VGS = 0 V, VDS = 16 V, f = 1 MHz		79	120	pF
Output Capacitance	Coss			13	20	
Reverse Transfer Capacitance	Crss			9	15	
<b>SWITCHING PARAMETERS (Note 4)</b>						
Turn-On DelayTime (Note 3)	td(on)	VGS = 10V, VDD = 4.5 V, Id = 0.5A, RGEN = 10 $\Omega$		6.7		ns
Turn-On Rise Time (Note 3)	tr			4.8		
Turn-Off DelayTime (Note 3)	td(off)			17.3		
Turn-Off Fall Time (Note 3)	tf			7.4		

Notes:

- Pulse Test : Pulse width=300  $\mu$ s, duty cycle $\leq$ 2%.
- Switching characteristics are independent of operating junction temperatures.
- Granted by design, not subject to producing.

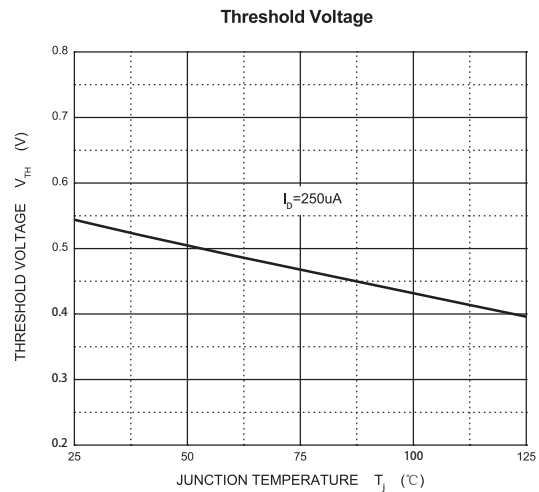
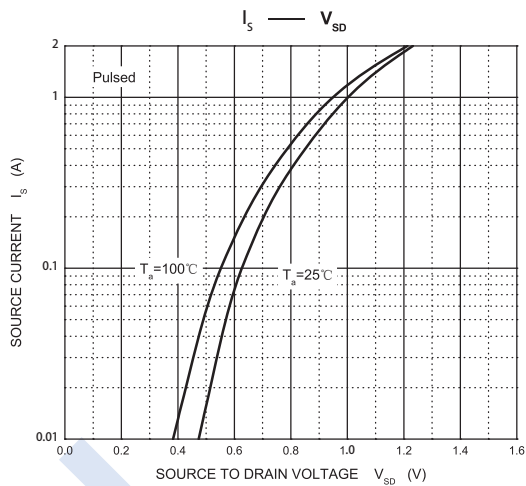
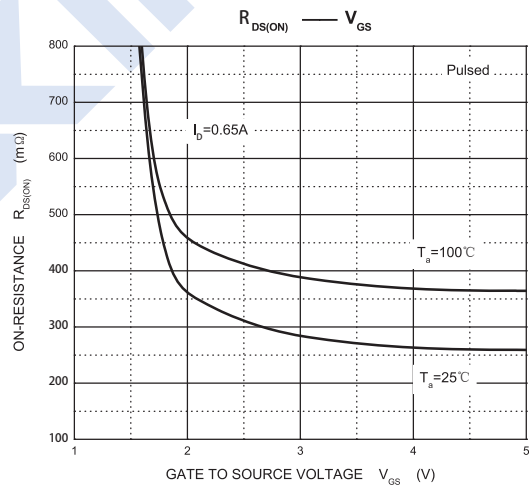
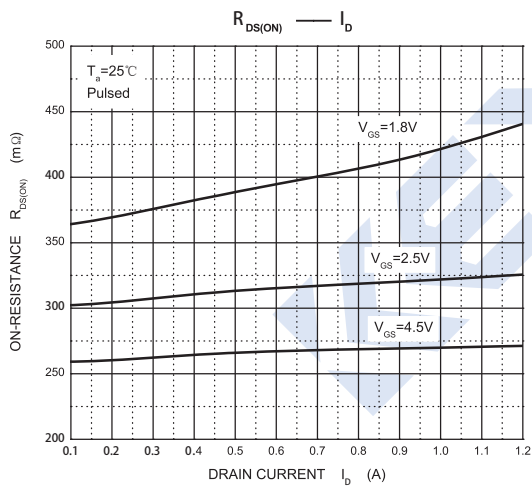
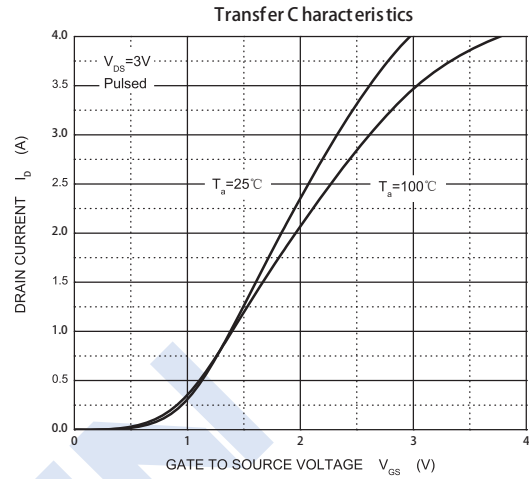
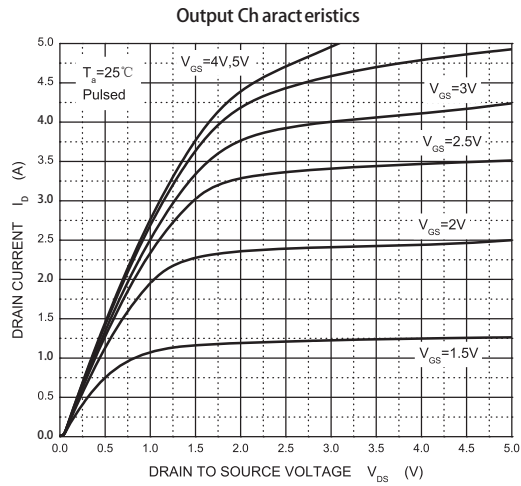
## ■ Marking

Marking	A
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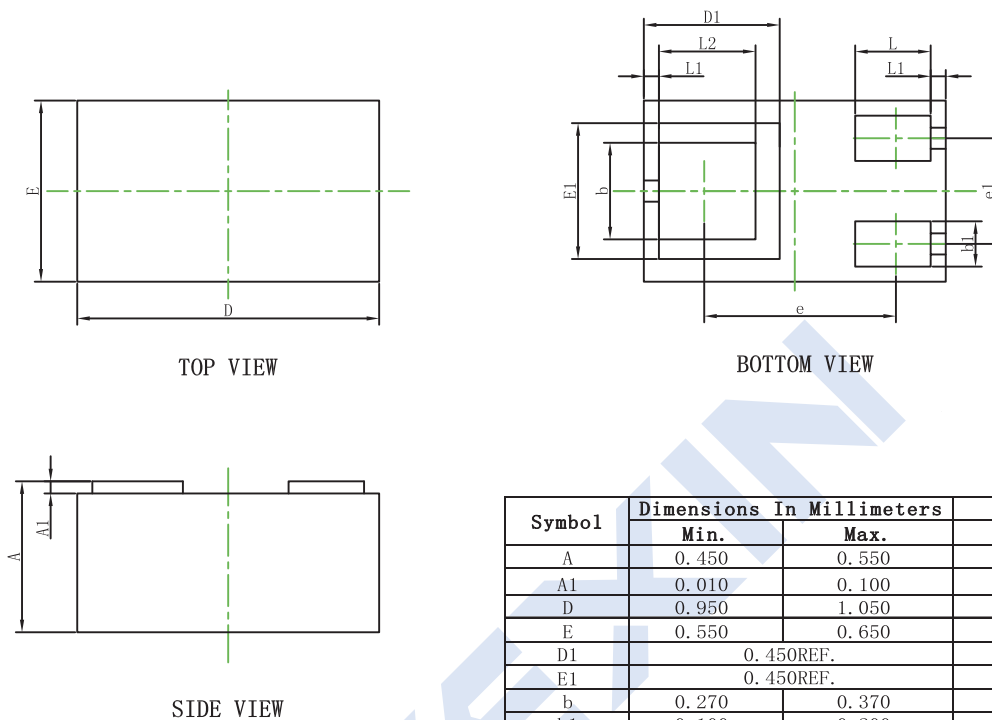
### Typical Characteristics



## N-Channel MOSFET

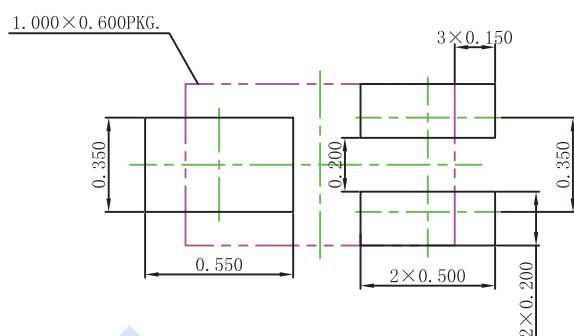
## 2KK6009DFN

## ■ DFN1006-3 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.450	0.550	0.018	0.022
A1	0.010	0.100	0.000	0.004
D	0.950	1.050	0.037	0.041
E	0.550	0.650	0.022	0.026
D1	0.450REF.		0.018REF.	
E1	0.450REF.		0.018REF.	
b	0.270	0.370	0.011	0.015
b1	0.100	0.200	0.004	0.008
e	0.635REF.		0.025REF.	
e1	0.300	0.400	0.012	0.016
L	0.200	0.300	0.008	0.012
L1	0.050REF.		0.002REF.	
L2	0.270	0.370	0.011	0.015

## ■ DFN1006-3 Suggested Pad Layout

**Note:**

1. Controlling dimension: in millimeters.
2. General tolerance:  $\pm 0.050$ mm.
3. The pad layout is for reference purposes only.