

Complementary MOSFET

2NP13

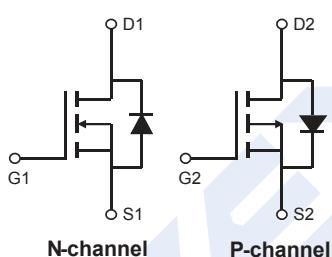
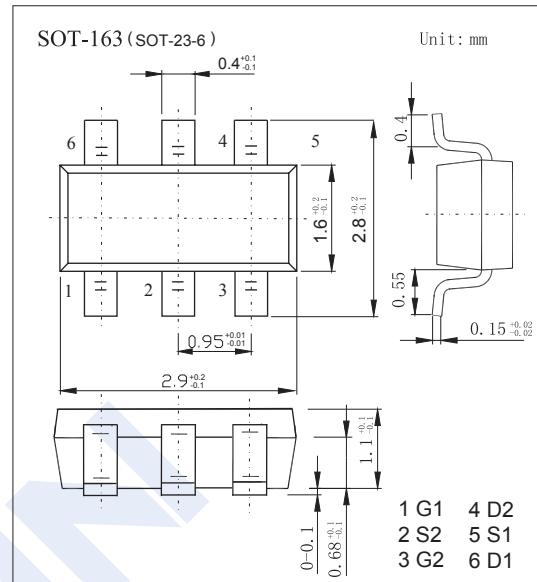
■ Features

N-Channel :

- $V_{DS(V)} = 30V$
- $I_D = 4.3 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 50m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 70m\Omega (V_{GS} = 4.5V)$

P-Channel :

- $V_{DS(V)} = -30V$
- $I_D = -4.1 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 52 m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 87 m\Omega (V_{GS} = -4.5V)$

■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	V_{DS}	30	-30	V
Gate-Source Voltage	V_{GS}	± 20		
Continuous Drain Current	I_D	4.3	-4.1	A
		3.7	-3.5	
Pulsed Drain Current	I_{DM}	20	-15	
Power Dissipation	P_D	1.15		W
		0.73		
Thermal Resistance.Junction- to-Ambient	R_{thJA}	110		$^\circ C/W$
		150		
Thermal Resistance.Junction- to-Lead	R_{thJL}	80		
Junction Temperature	T_J	150		
Storage Temperature Range	T_{stg}	-55 to 150		$^\circ C$

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■ N-Channel Mosfet Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 250 μA	30			V
Gate threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1		3	
Gate-body leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			±100	nA
Zero gate voltage drain current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0 V			0.5	uA
		V _{DS} = 30V, V _{GS} = 0 V, T _J = 55 °C			10	
On-state drain current	I _{D(on)}	V _{DS} ≥ 4.5 V, V _{GS} = 10 V	6			A
		V _{DS} ≥ 4.5 V, V _{GS} = 4.5 V	4			
Drain-source on-state resistance	r _{DS(on)}	V _{GS} = 10 V, I _D = 3.5 A			50	mΩ
		V _{GS} = 4.5 V, I _D = 2.8 A			70	
Forward transconductance	g _f s	V _{DS} = 4.5 V, I _D = 3.5 A		6.9		S
Diode forward voltage	V _{SD}	I _S = 1.25 A, V _{GS} = 0 V		0.8	1.2	V
gate charge *	Q _g	V _{DS} = 15V , V _{GS} = 5V , I _D = 3.5 A		4.2	7	nC
Total gate charge *	Q _{gt}			8.5	20	nC
Gate-source charge *	Q _{gs}	V _{DS} = 15V , V _{GS} = 10 V , I _D = 3.5 A		1.9		
Gate-drain charge *	Q _{gd}			1.35		
Gate Resistance	R _g		0.5		2.4	Ω
Input capacitance *	C _{iss}	V _{DS} = 15V , V _{GS} = 0 , f = 1 MHz		555		pF
Output capacitance *	C _{oss}			120		
Reverse transfer capacitance *	C _{rss}			60		
Turn-on time	t _{d(on)}	V _{DD} = 15V , R _L = 15 Ω, I _D = 1A , V _{GEN} = -10V , R _G = 6Ω		9	20	ns
	t _r			7.5	18	
Turn-off time	t _{d(off)}			17	35	
	t _f			5.2	12	

* Pulse test: PW ≤ 300 us duty cycle ≤ 2%.

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■ P-Channel Mosfet Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=-250 \mu\text{A}, V_{GS}=0\text{V}$	-30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-24\text{V}, V_{GS}=0\text{V}$		-1		μA
		$V_{DS}=-24\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$		-5		
Gate-Body leakage current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS}=V_{GS}, I_D=-250 \mu\text{A}$	-1		-3	V
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	$V_{GS}=-10\text{V}, I_D=-4.1\text{A}$			52	$\text{m}\Omega$
		$V_{GS}=-10\text{V}, I_D=-4\text{A}, T_J=125^\circ\text{C}$			73	
		$V_{GS}=-4.5\text{V}, I_D=-3\text{A}$			87	
On state drain current	$I_{D(\text{ON})}$	$V_{GS}=-4.5\text{V}, V_{DS}=-5\text{V}$	-10			A
Forward Transconductance	g_{FS}	$V_{DS}=-5\text{V}, I_D=-4\text{A}$	5.5	8.2		S
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=-15\text{V}, f=1\text{MHz}$		700		pF
Output Capacitance	C_{oss}			120		
Reverse Transfer Capacitance	C_{rss}			75		
Gate resistance	R_g	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$		10		Ω
Total Gate Charge	Q_g	$V_{GS}=-4.5\text{V}, V_{DS}=-15\text{V}, I_D=-4\text{A}$		14.3		nC
Gate Source Charge	Q_{gs}			7		
Gate Drain Charge	Q_{gd}			3.1		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=-10\text{V}, V_{DS}=-15\text{V}, R_L=3.6\Omega, R_{GEN}=3\Omega$		8.6		ns
Turn-On Rise Time	t_r			5		
Turn-Off Delay Time	$t_{d(off)}$			28.2		
Turn-Off Fall Time	t_f			13.5		
Body Diode Reverse Recovery Time	t_{rr}	$ I_F =-4\text{A}, dI/dt=100\text{A}/\mu\text{s}$		27		nC
Body Diode Reverse Recovery Charge	Q_{rr}			15		
Maximum Body-Diode Continuous Current	I_S				-2.2	A
Diode Forward Voltage	V_{SD}	$I_S=-1\text{A}, V_{GS}=0\text{V}$		-0.77	-1	V

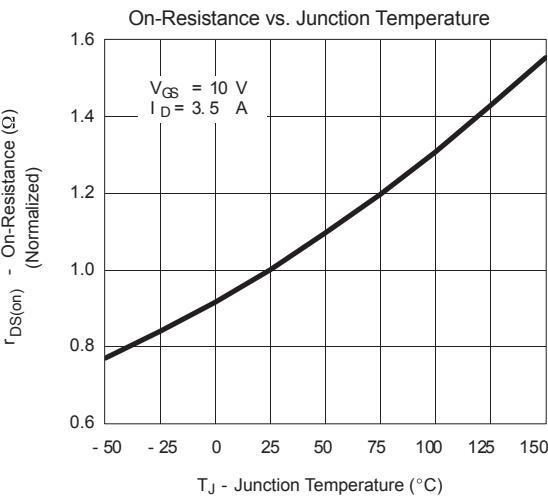
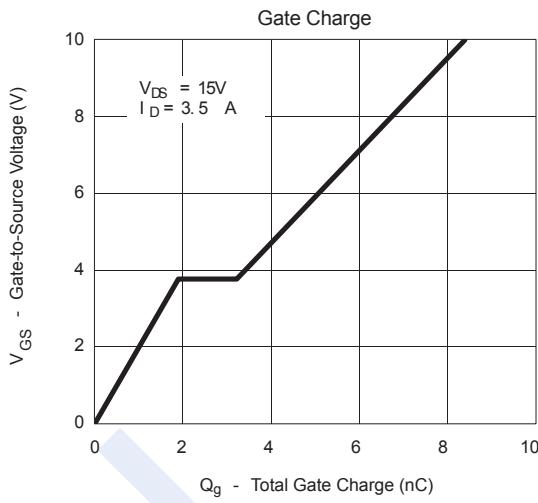
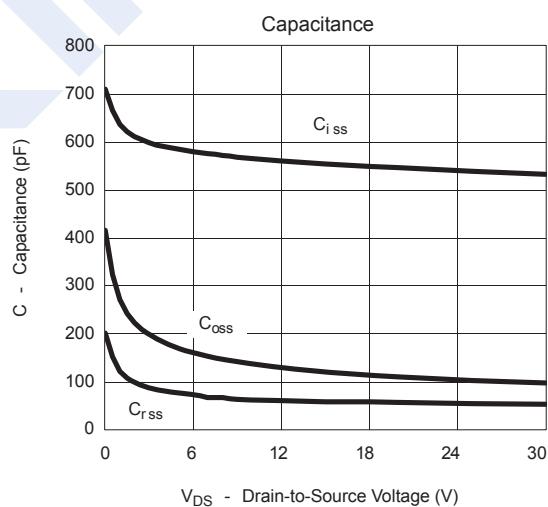
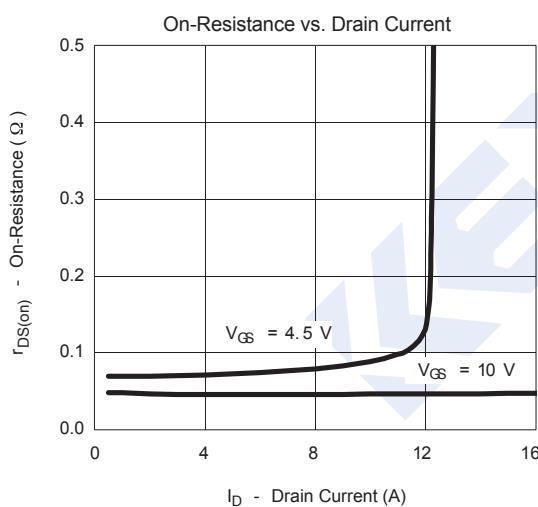
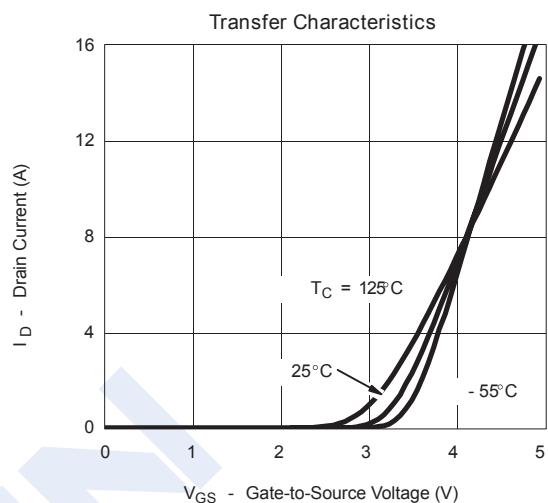
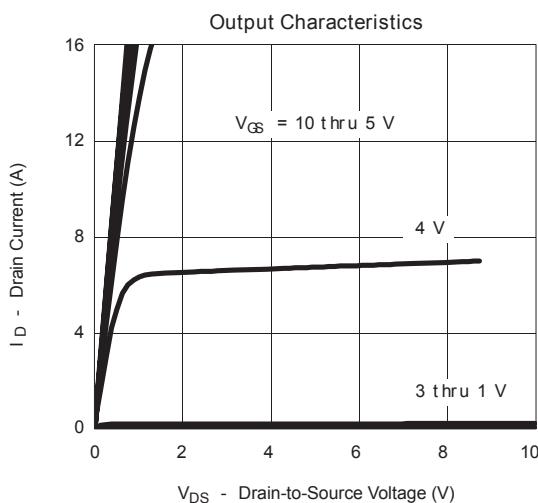
■ Marking

Marking	NP13 *
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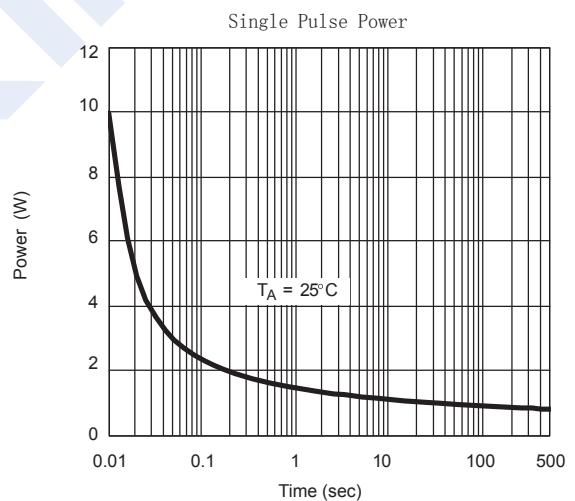
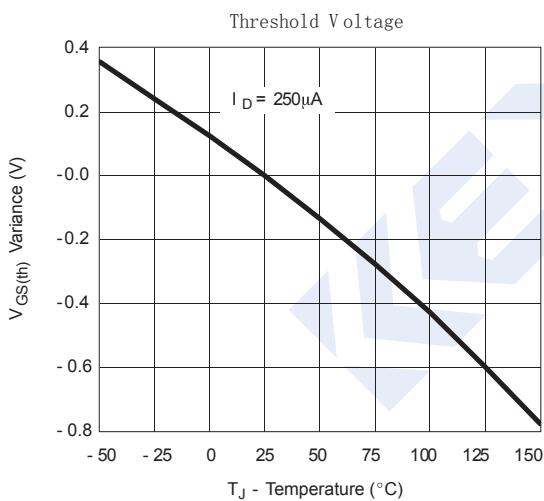
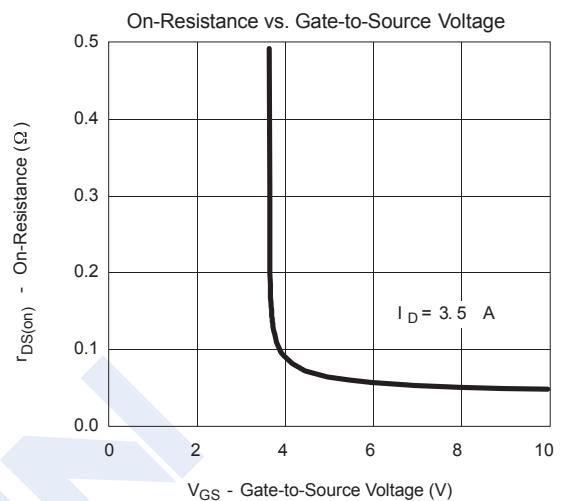
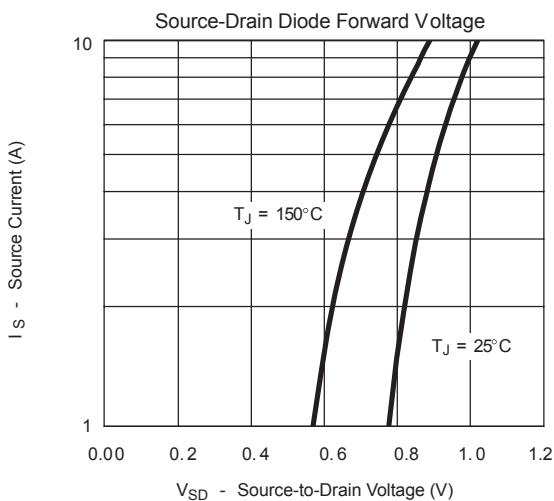
■ N-Channel Mosfet Typical Characteristics



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■ N-Channel Mosfet Typical Characteristics



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■ P-Channel Mosfet Typical Characteristics

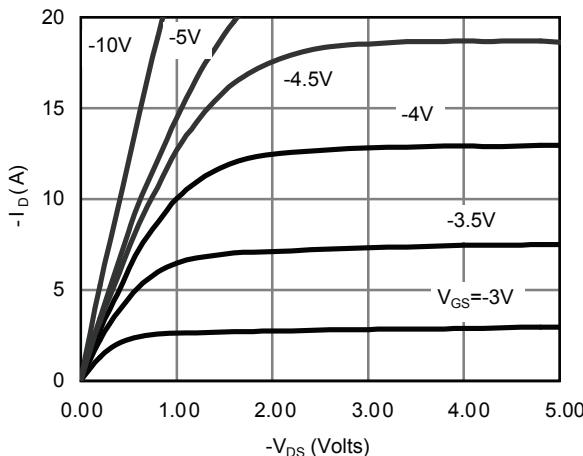


Figure 1: On-Region Characteristics

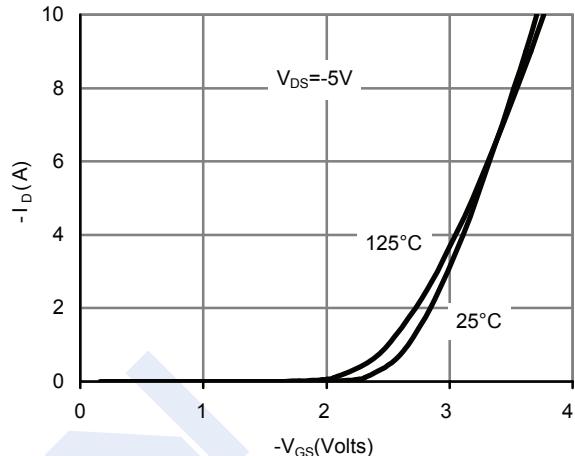


Figure 2: Transfer Characteristics

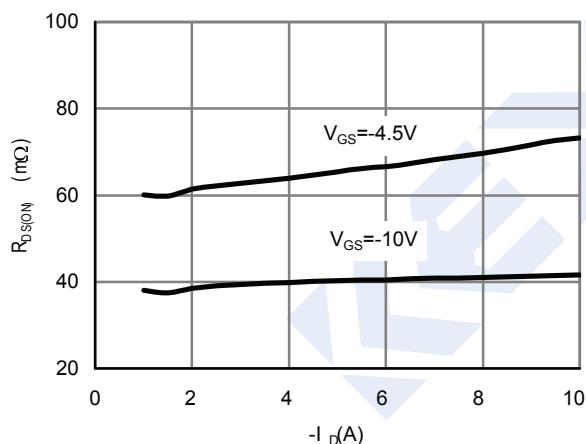


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

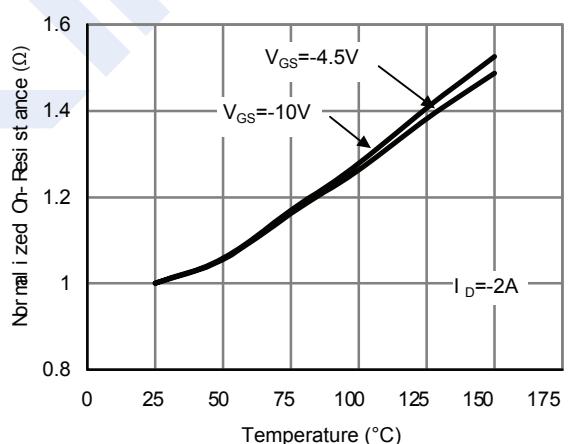


Figure 4: On-Resistance vs. Junction Temperature

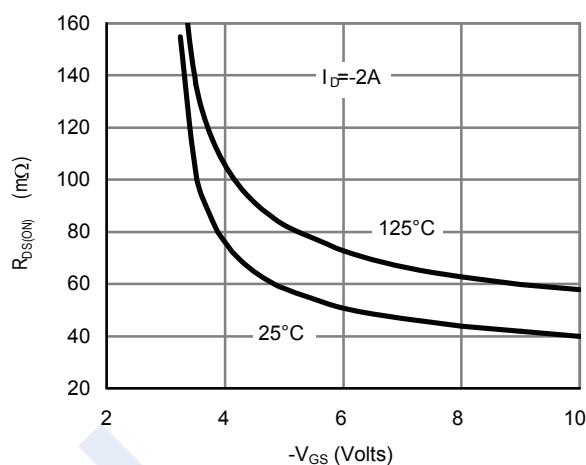


Figure 5: On-Resistance vs. Gate-Source Voltage

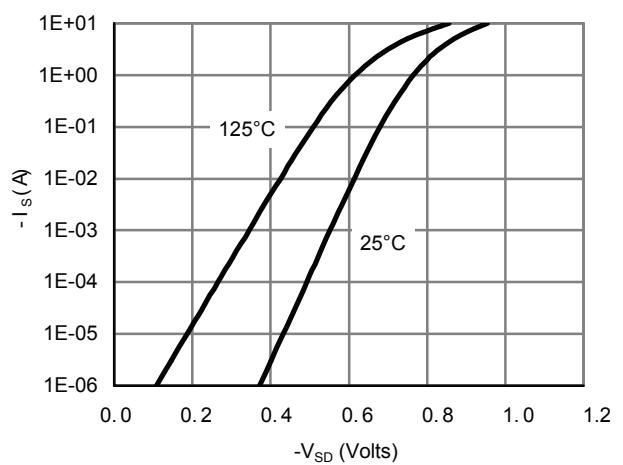


Figure 6: Body-Diode Characteristics

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■ P-Channel Mosfet Typical Characteristics

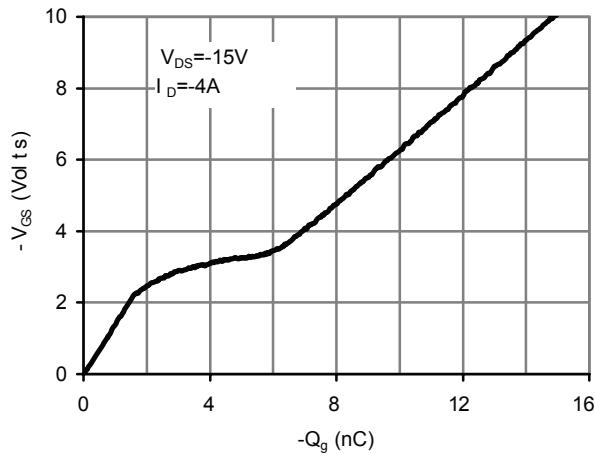


Figure 7: Gate-Charge Characteristics

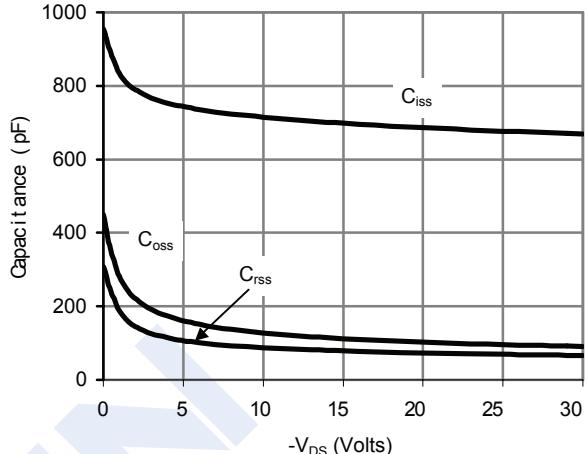


Figure 8: Capacitance Characteristics

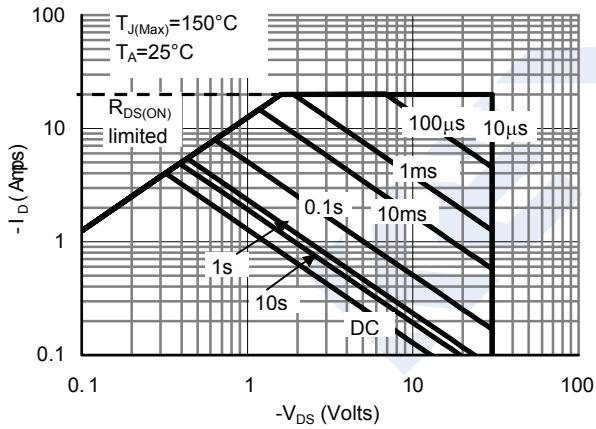


Figure 9: Maximum Forward Biased Safe Operating Area

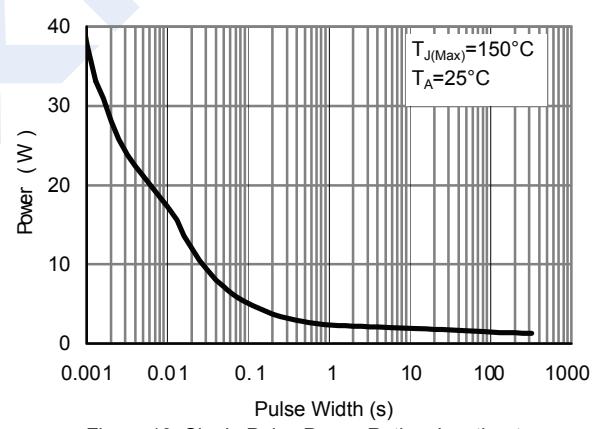


Figure 10: Single Pulse Power Rating Junction-to-Ambient