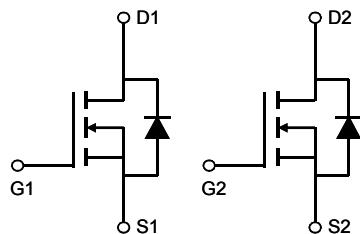
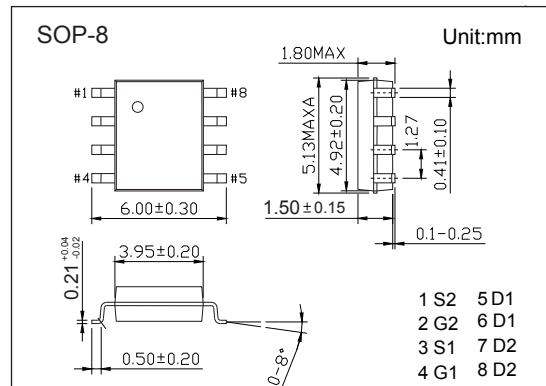


## Dual N-Channel MOSFET

### AO4832 (KO4832)

#### ■ Features

- $V_{DS} (V) = 30V$
- $I_D = 10A$  ( $V_{GS} = 10V$ )
- $R_{DS(ON)} < 13m\Omega$  ( $V_{GS} = 10V$ )
- $R_{DS(ON)} < 17.5m\Omega$  ( $V_{GS} = 4.5V$ )



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current	$T_a=25^\circ C$	$I_D$	10	A
	$T_a=70^\circ C$		8	
Pulsed Drain Current		$I_{DM}$	55	A
Avalanche Current		$I_{AS}, I_{AR}$	22	
Repetitive Avalanche Energy	$L=0.1mH$	$E_{AS}, E_{AR}$	24	mJ
Power Dissipation	$T_a=25^\circ C$	$P_D$	2	W
	$T_a=70^\circ C$		1.3	
Thermal Resistance.Junction- to-Ambient	$t \leqslant 10s$	$R_{thJA}$	62.5	$^\circ C/W$
	Steady-State		90	
Thermal Resistance.Junction- to-Lead		$R_{thJL}$	40	$^\circ C$
Junction Temperature		$T_J$	150	
Storage Temperature Range		$T_{stg}$	-55 to 150	

## Dual N-Channel MOSFET

### AO4832 (KO4832)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =250 μ A, V <sub>GS</sub> =0V	30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			5	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.5		2.5	V
Static Drain-Source On-Resistance	R <sub>Ds(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =10A			13	mΩ
		V <sub>GS</sub> =10V, I <sub>D</sub> =10A, T <sub>J</sub> =125°C			19	
		V <sub>GS</sub> =4.5 V, I <sub>D</sub> =8A			17.5	
On State Drain Current	I <sub>D(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =5V	55			A
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =10A		43		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =15V, f=1MHz	610		910	pF
Output Capacitance	C <sub>oss</sub>		88		160	
Reverse Transfer Capacitance	C <sub>rss</sub>		40		100	
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz	0.8		2.4	Ω
Total Gate Charge (10V)	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V, I <sub>D</sub> =10A	11		17	nC
Total Gate Charge (4.5V)	Q <sub>gs</sub>		5		8	
Gate Source Charge	Q <sub>gd</sub>			2.4		
Gate Drain Charge	Q <sub>gd</sub>			3		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =15V, R <sub>L</sub> =1.5Ω, R <sub>GEN</sub> =3Ω		4.4		ns
Turn-On Rise Time	t <sub>r</sub>			9		
Turn-Off DelayTime	t <sub>d(off)</sub>			17		
Turn-Off Fall Time	t <sub>f</sub>			6		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 10A, d <sub>i</sub> /d <sub>t</sub> = 500A/us	5.6		8	nC
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		6.4		9.6	
Maximum Body-Diode Continuous Current	I <sub>s</sub>				2.5	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>s</sub> =1A, V <sub>GS</sub> =0V			1	V

Note. The static characteristics in Figures 1 to 6 are obtained using <300us pulses, duty cycle 0.5% max.

■ Marking

Marking	4832
	KA****

## Dual N-Channel MOSFET

### AO4832 (KO4832)

■ Typical Characteristics

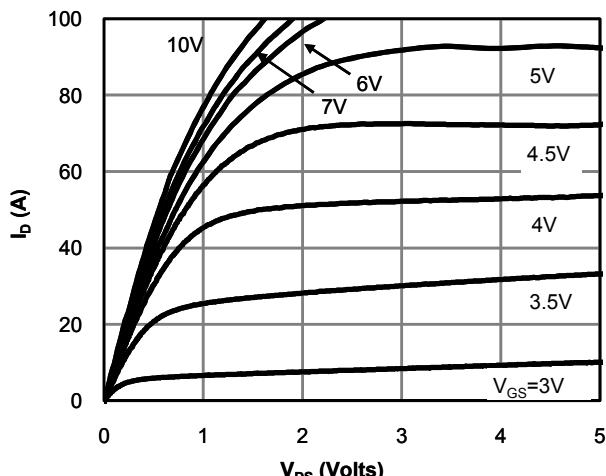


Fig 1: On-Region Characteristics (Note E)

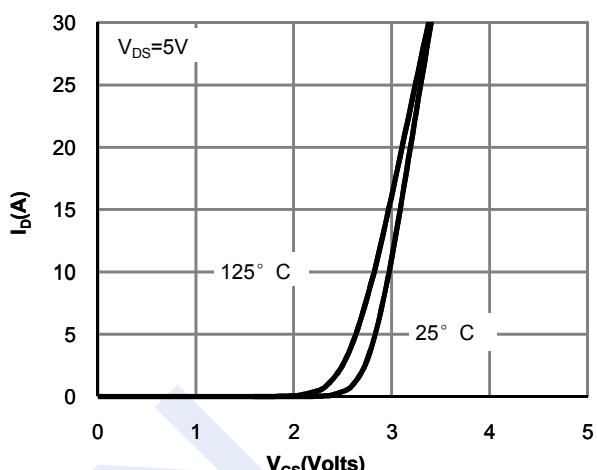


Figure 2: Transfer Characteristics (Note E)

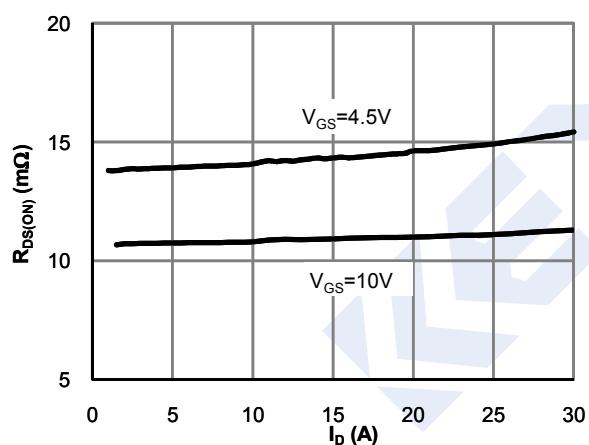


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

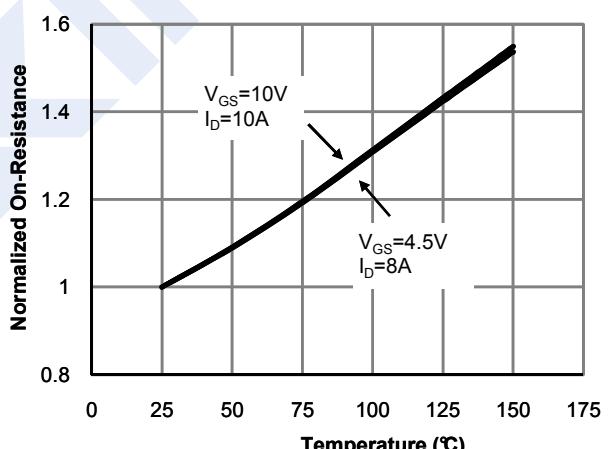


Figure 4: On-Resistance vs. Junction Temperature (Note E)

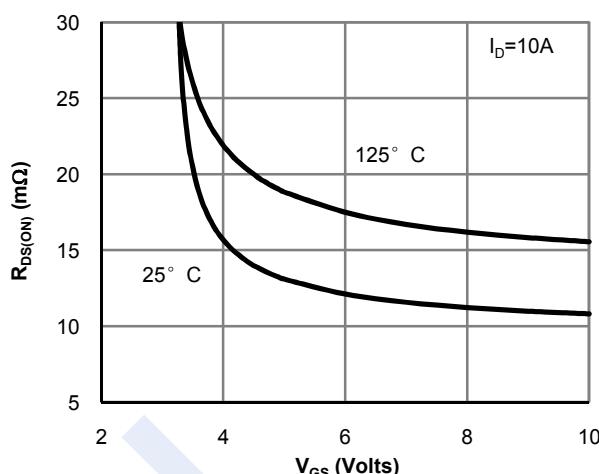


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

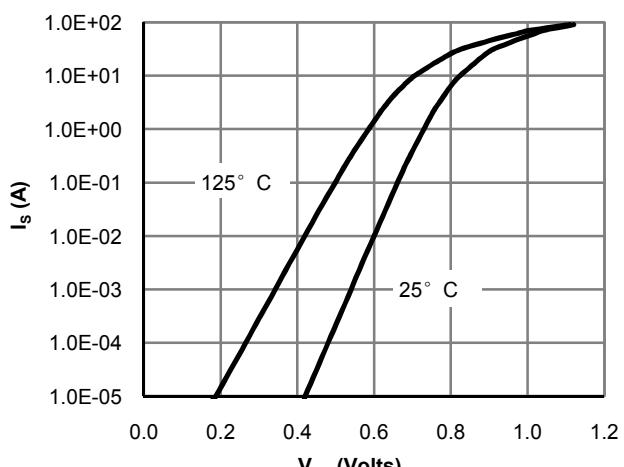


Figure 6: Body-Diode Characteristics (Note E)

## Dual N-Channel MOSFET

### AO4832 (KO4832)

■ Typical Characteristics

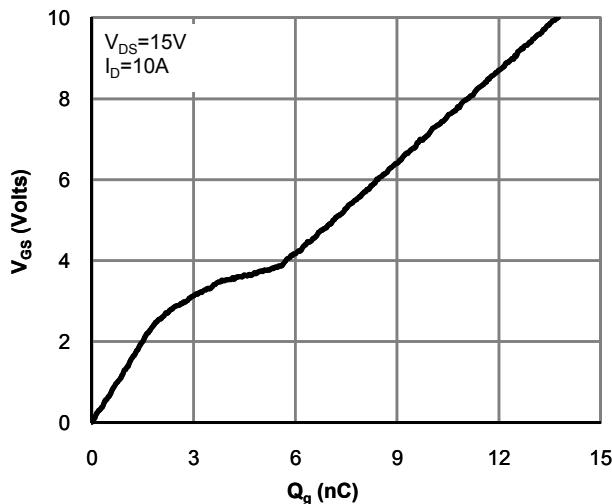


Figure 7: Gate-Charge Characteristics

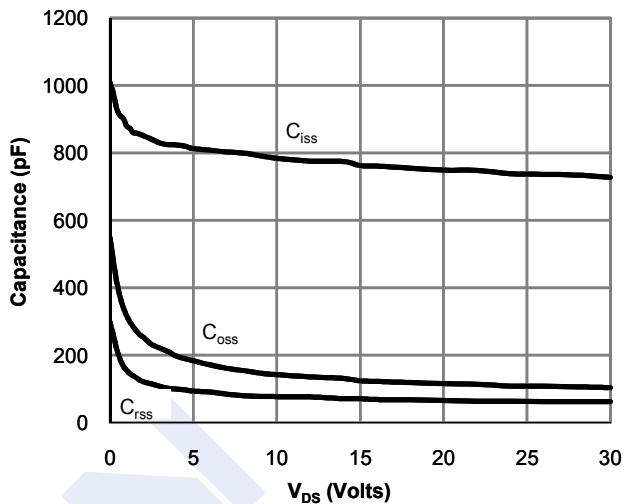


Figure 8: Capacitance Characteristics

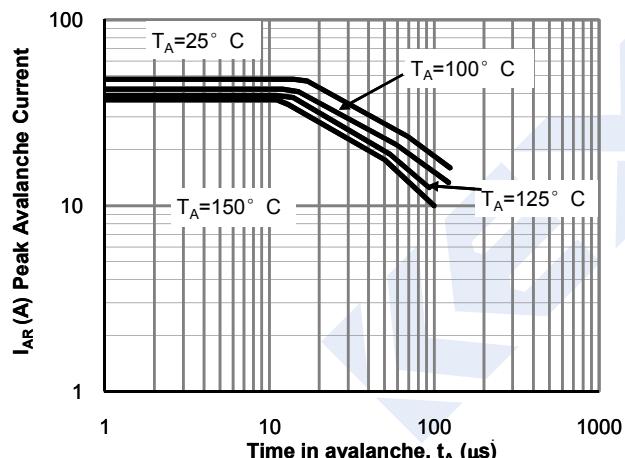


Figure 9: Single Pulse Avalanche capability (Note C)

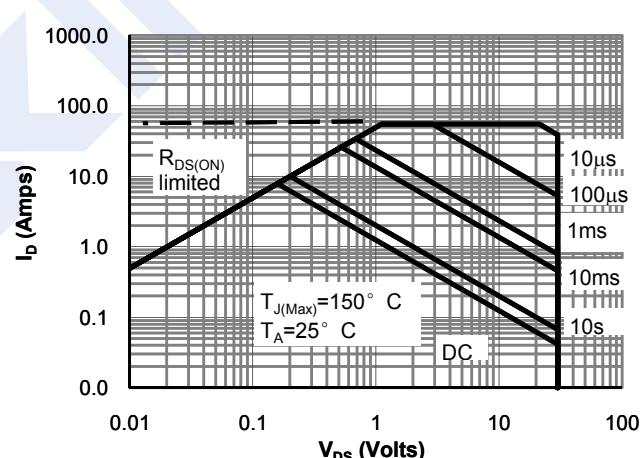


Figure 10: Maximum Forward Biased Safe Operating Area (Note F)

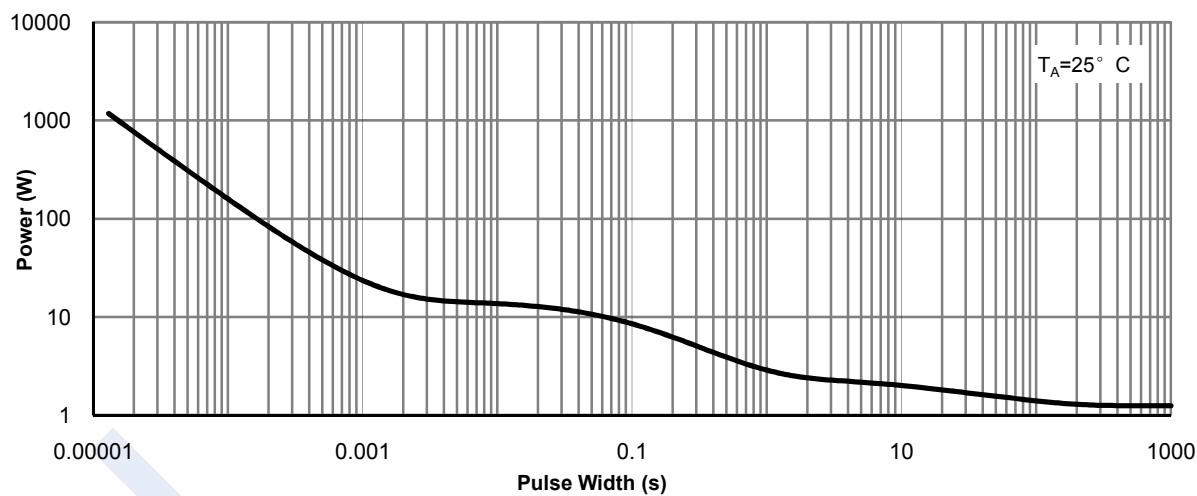


Figure 11: Single Pulse Power Rating Junction-to-Ambient (Note F)

## Dual N-Channel MOSFET

### AO4832 (KO4832)

#### ■ Typical Characteristics

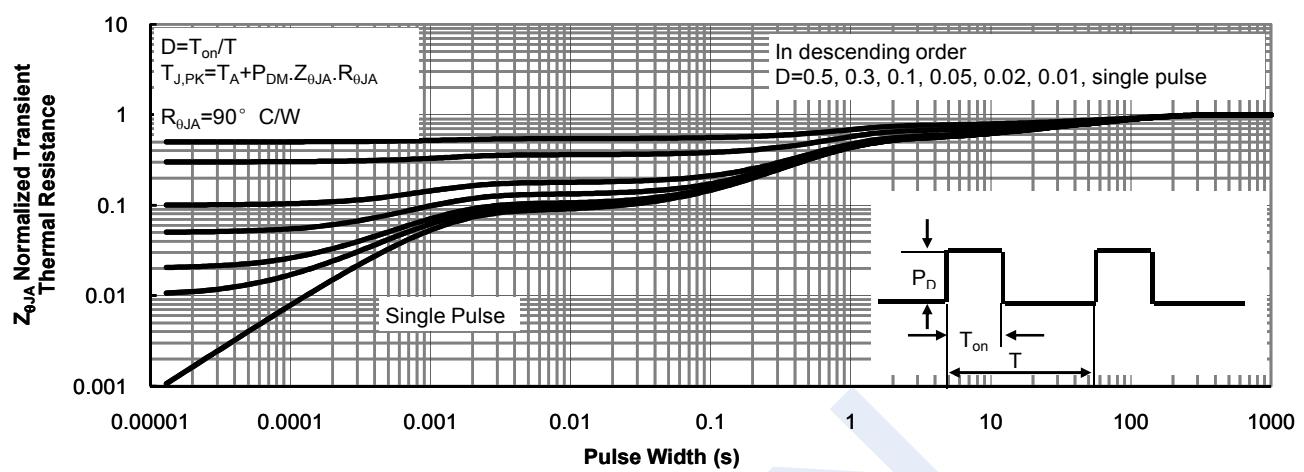


Figure 12: Normalized Maximum Transient Thermal Impedance (Note F)