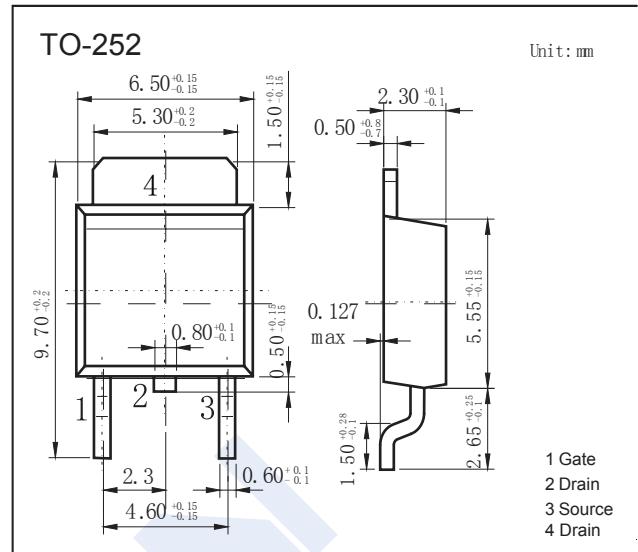


## N-Channel Enhancement MOSFET

### AOD464 (KOD464)

#### ■ Features

- $V_{DS}$  (V) = 105V
- $I_D$  = 40A ( $V_{GS}$  = 10V)
- $R_{DS(ON)} < 28m\Omega$  ( $V_{GS}$  = 10V) @20A
- $R_{DS(ON)} < 31m\Omega$  ( $V_{GS}$  = 6V)



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	105	V
Gate-Source Voltage	$V_{GS}$	$\pm 25$	
Continuous Drain Current	$I_C$ TC=25°C	40	A
	$I_C$ TC=100°C	28	
Pulsed Drain Current	$I_{DM}$	80	A
Avalanche Current	$I_{AR}$	20	
Repetitive avalanche energy L=0.1mH	$E_{AR}$	20	mJ
Power Dissipation	$P_D$ TC=25°C	100	W
	$P_D$ TC=100°C	50	
Power Dissipation	$P_{DSM}$ TA=25°C	2.3	°C/W
	$P_{DSM}$ TA=70°C	1.5	
Thermal Resistance.Junction- to-Ambient t ≤ 10s	$R_{θJA}$	18	°C/W
Thermal Resistance.Junction- to-Ambient	$R_{θJA}$	55	
Thermal Resistance.Junction- to-Case	$R_{θJC}$	1.5	°C
Junction Temperature	$T_J$	175	
Storage Temperature Range	$T_{stg}$	-55 to 175	

## N-Channel Enhancement MOSFET

### AOD464 (KOD464)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =10mA, V <sub>GS</sub> =0V	105			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =84V, V <sub>GS</sub> =0V			1	μ A
		V <sub>DS</sub> =84V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			5	
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±25V			100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250 μ A	2.5		4	V
Static Drain-Source On-Resistance	R <sub>Ds(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A			28	m Ω
		V <sub>GS</sub> =10V, I <sub>D</sub> =20A T <sub>J</sub> =125°C			40	
		V <sub>GS</sub> =6V, I <sub>D</sub> =20A			31	
On State Drain Current	I <sub>D(ON)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =5V	80			A
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =20A		50		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz		2038	2445	pF
Output Capacitance	C <sub>oss</sub>			204		
Reverse Transfer Capacitance	C <sub>rss</sub>			85		
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, f=1MHz			1.56	Ω
Total Gate Charge	Q <sub>g(10V)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, I <sub>D</sub> =20A		38.5	46	nC
Gate Source Charge	Q <sub>gs</sub>			8		
Gate Drain Charge	Q <sub>gd</sub>			10		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =50V, R <sub>L</sub> =2.7 Ω ,R <sub>GEN</sub> =3 Ω		12.7		ns
Turn-On Rise Time	t <sub>r</sub>			8.2		
Turn-Off DelayTime	t <sub>d(off)</sub>			31.5		
Turn-Off Fall Time	t <sub>f</sub>			11.2		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A, dI/dt= 100A/ μ s		59.6	74	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> = 20A, dI/dt= 100A/ μ s		161		nC
Maximum Body-Diode Continuous Current	I <sub>s</sub>				55	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>s</sub> =1A,V <sub>GS</sub> =0V			1	V

## N-Channel Enhancement MOSFET

### AOD464 (KOD464)

#### ■ Typical Characteristics

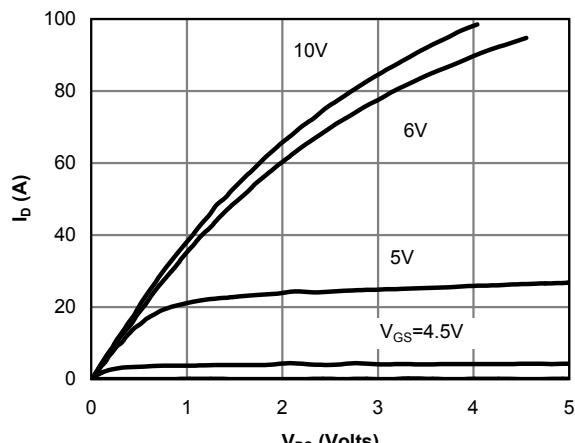


Fig 1: On-Region Characteristics

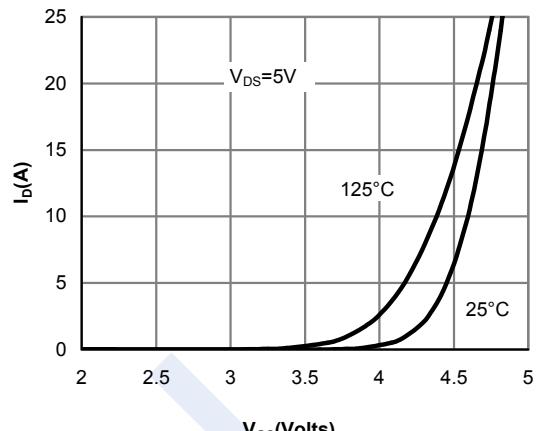


Figure 2: Transfer Characteristics

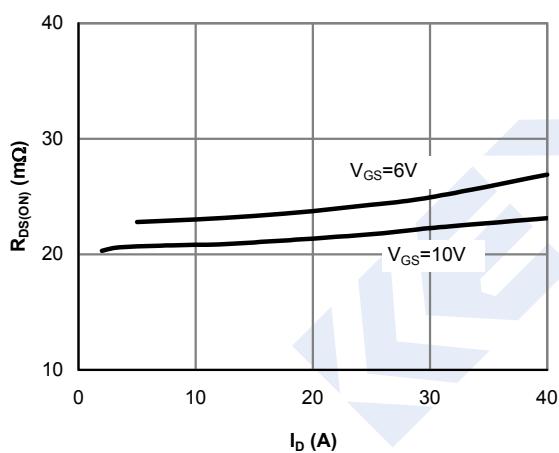


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

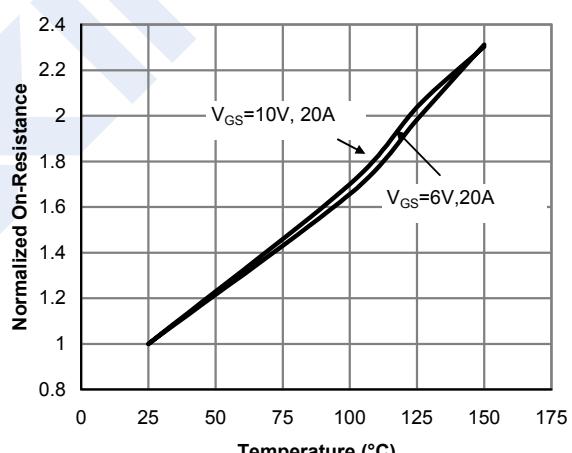


Figure 4: On-Resistance vs. Junction Temperature

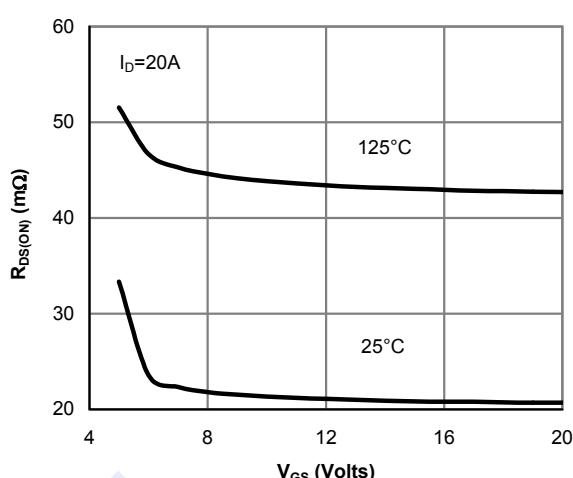


Figure 5: On-Resistance vs. Gate-Source Voltage

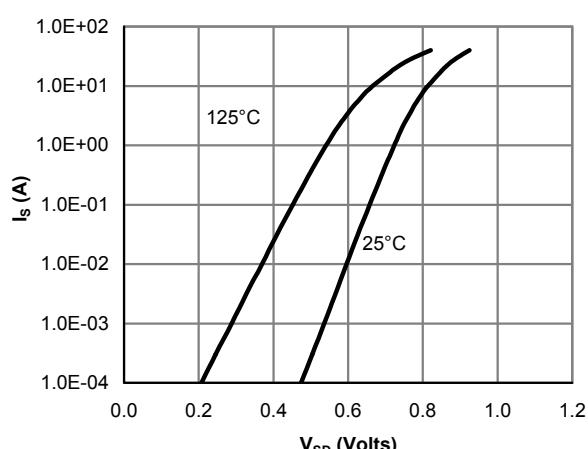


Figure 6: Body-Diode Characteristics

## N-Channel Enhancement MOSFET

AOD464 (KOD464)

## ■ Typical Characteristics

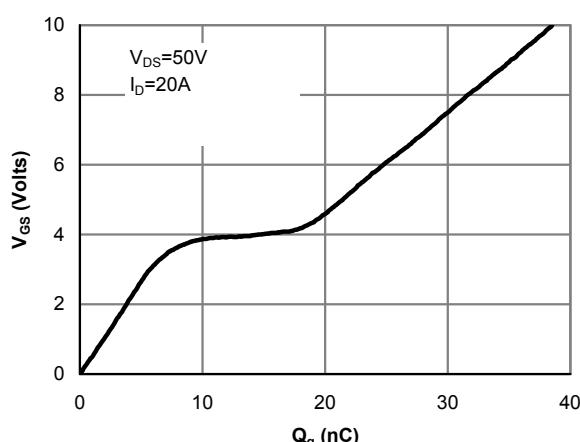
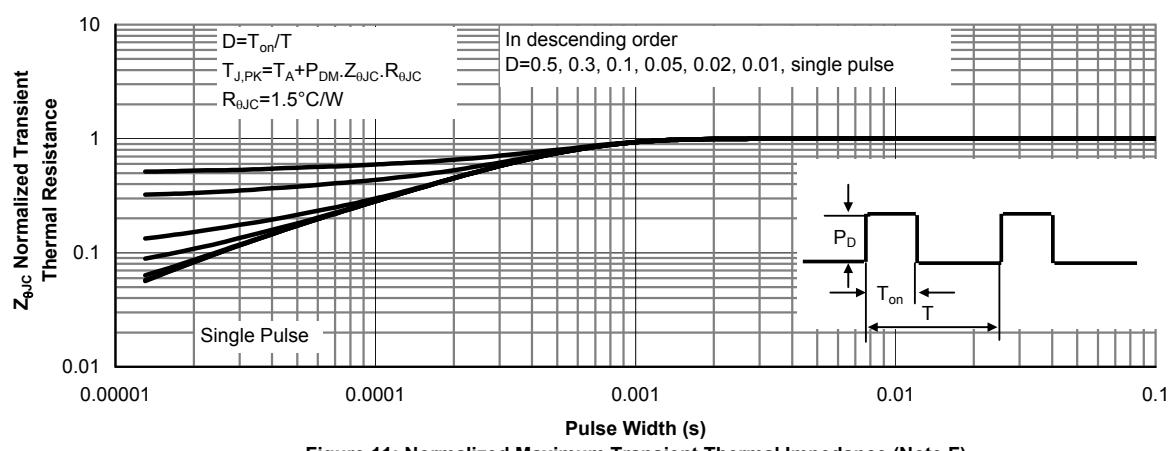
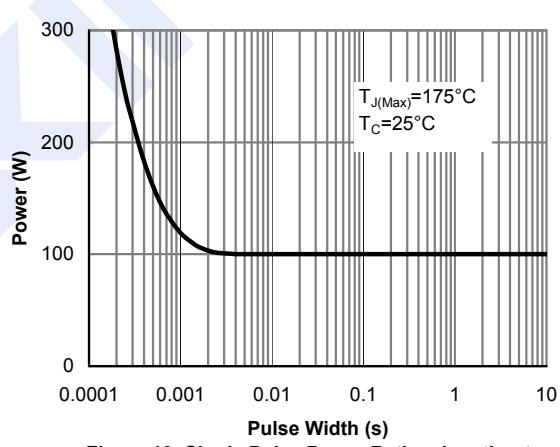
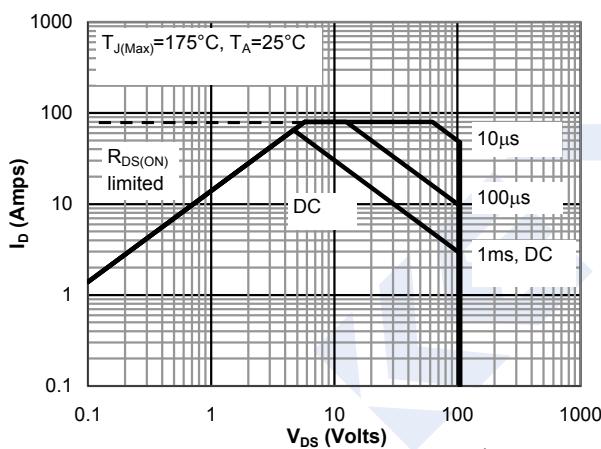
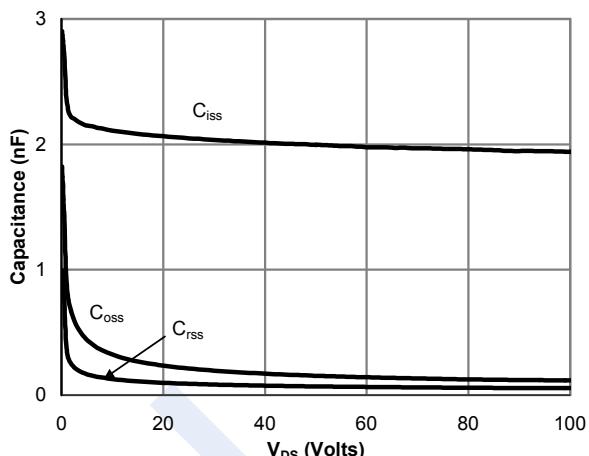


Figure 7: Gate-Charge Characteristics



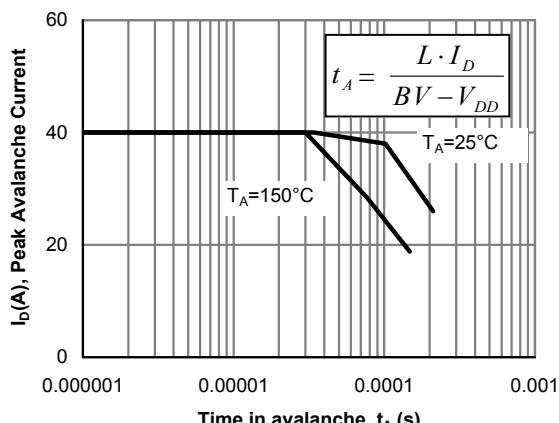
**N-Channel Enhancement MOSFET****AOD464 (KOD464)****■ Typical Characteristics**

Figure 12: Single Pulse Avalanche capability

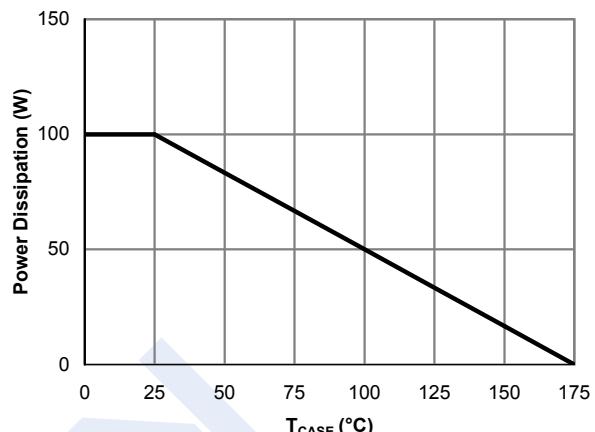


Figure 13: Power De-rating (Note B)

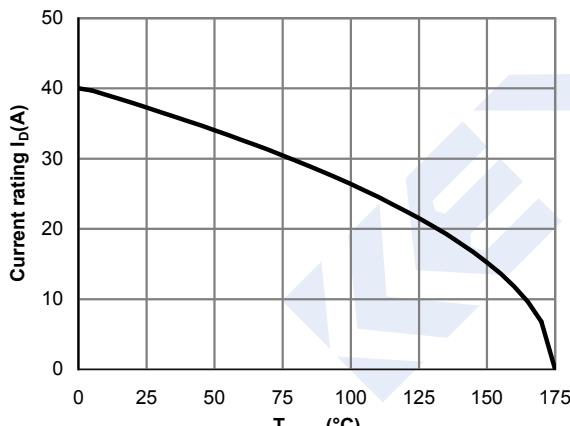


Figure 14: Current De-rating (Note B)

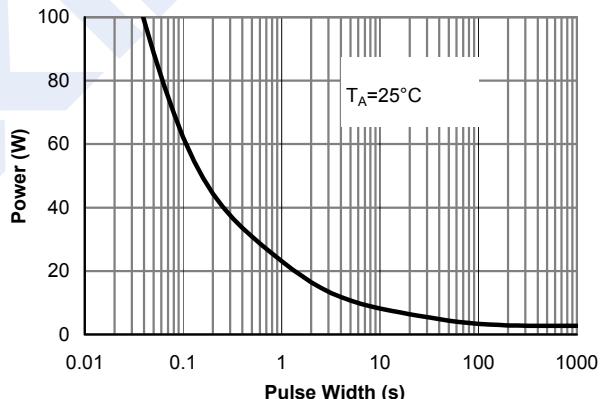


Figure 15: Single Pulse Power Rating Junction-to-Ambient (Note H)

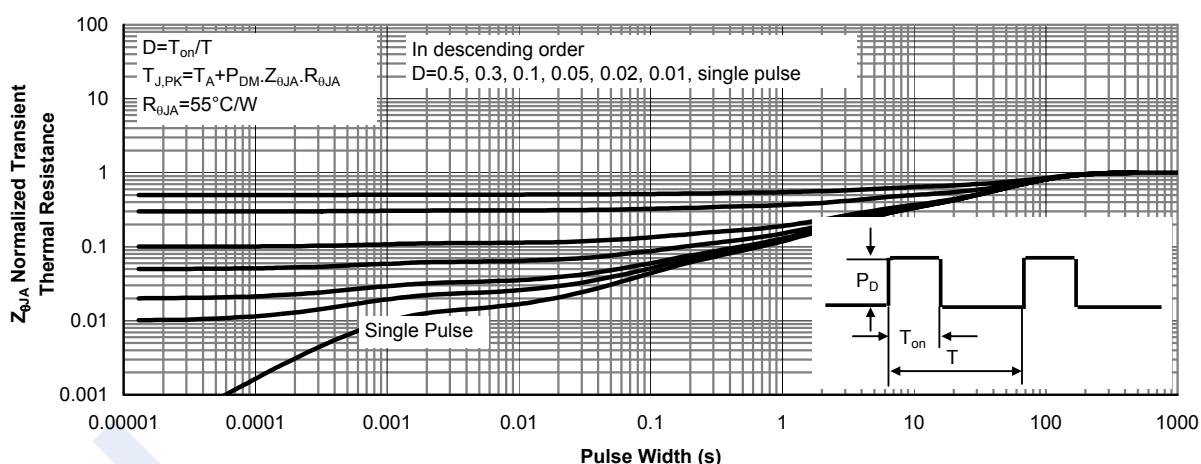


Figure 16: Normalized Maximum Transient Thermal Impedance (Note H)