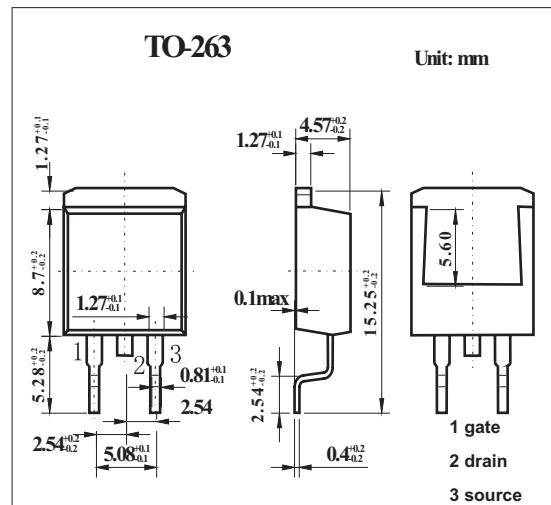
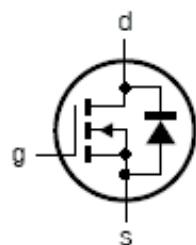


## TrenchMOS™ transistor Standard level FET

### KUK7605-30A

#### ■ Features

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#### ■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit
Drain-source voltage	V <sub>DS</sub>	30	V
Drain-gate voltage R <sub>GS</sub> = 20 kΩ	V <sub>DGR</sub>	30	V
Gate-source voltage	±V <sub>GS</sub>	20	V
Drain current (DC) T <sub>mb</sub> = 25°C	I <sub>D</sub>	75	A
Drain current (DC) T <sub>mb</sub> = 100°C	I <sub>D</sub>	75	A
Drain current (pulse peak value) T <sub>mb</sub> = 25°C	I <sub>DM</sub>	400	A
Total power dissipation T <sub>mb</sub> = 25°C	P <sub>tot</sub>	230	W
Storage & operating temperature	T <sub>stg</sub> , T <sub>j</sub>	-55 to 175	°C
Thermal resistance junction to mounting base	R <sub>th j-mb</sub>	0.65	K/W
Thermal resistance junction to ambient	R <sub>th j-a</sub>	50	K/W

**KUK7605-30A**■ Electrical Characteristics  $T_a = 25^\circ\text{C}$ 

Parameter	Symbol	Testconditons	Min	Typ	Max	Unit
drain-source breakdown voltage	$V_{(BR)DSS}$	$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = 25^\circ\text{C}$	30			V
		$I_D = 0.25 \text{ mA}; V_{GS} = 0 \text{ V}; T_j = -55^\circ\text{C}$	27			V
gate-source threshold voltage	$V_{GS(th)}$	$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 25^\circ\text{C}$	2	3	4	V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = 175^\circ\text{C}$	1			V
		$I_D = 1 \text{ mA}; V_{DS} = V_{GS}; T_j = -55^\circ\text{C}$			4.4	V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25^\circ\text{C}$		0.05	10	$\mu\text{A}$
		$V_{DS} = 30 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 175^\circ\text{C}$			500	$\mu\text{A}$
gate-source leakage current	$I_{GSS}$	$V_{GS} = \pm 20 \text{ V}; V_{DS} = 0 \text{ V}$	2	100	nA	
drain-source on-state resistance	$R_{DS(on)}$	$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 25^\circ\text{C}$	4.3	5	mΩ	
		$V_{GS} = 10 \text{ V}; I_D = 25 \text{ A}; T_j = 175^\circ\text{C}$			9.3	mΩ
input capacitance	$C_{iss}$	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz}$		4500	6000	pF
output capacitance	$C_{oss}$			1500	1800	pF
reverse transfer capacitance	$C_{rss}$			960	1300	pF
turn-on delay time	$t_{d(on)}$	$V_{DD} = 30 \text{ V}; R_L = 1.2\Omega; V_{GS} = 10 \text{ V}; R_G = 10\Omega$		35	55	ns
rise time	$t_r$			130	200	ns
turn-off delay time	$t_{d(off)}$			155	230	ns
fall time	$t_f$			150	220	ns
internal drain inductance	$L_d$	from drain lead 6 mm from package to centre of die		2.5		nH
internal source inductance	$L_s$	Measured from source lead soldering point to source bond pad		7.5		nH
Continuous reverse drain current	$I_{DR}$				75	A
Pulsed reverse drain current	$I_{DRM}$				240	A
source-drain (diode forward) voltage	$V_{SD}$	$I_F = 25 \text{ A}; V_{GS} = 0 \text{ V}$	0.85	1.2	V	
		$I = 75 \text{ A}; V = 0 \text{ V}$		1.1		V
reverse recovery time	$t_{rr}$	$I_S = 75 \text{ A}; -dI_F/dt = 100 \text{ A}/\mu\text{s};$	400			ns
recovered charge	$Q_r$	$V_{GS} = -10 \text{ V}; V_{DS} = 30 \text{ V}$		1.0		$\mu\text{C}$
Drain-source non-repetitive unclamped inductive turn-off energy	$W_{DSS}$	$I_D=75\text{A};V_{DD}\leqslant25\text{V};V_{GS}=10\text{V};R_{GS}=50\Omega;T_{mb}=25^\circ\text{C}$			500	mJ